



Atty. Dkt. No. 029471-0168

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Hiroyuki TAKAHASHI et al.

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to enter  
7/18/07  
JH

Title: SEMICONDUCTOR MEMORY  
DEVICE AND  
SEMICONDUCTOR MEMORY  
DEVICE CONTROL METHOD

Appl. No.: 10/507,117

Filing Date: 9/10/2004

Examiner: Jung H. Hur

Art Unit: 2824

Confirmation 4725  
Number:

**AMENDMENT AND REPLY UNDER 37 CFR 1.116**

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

This communication is responsive to the Final Office Action dated May 10, 2007, concerning the above-referenced patent application.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this document.

**Remarks/Arguments** begin on page 21 of this document.

Please amend the application as follows: